

10/046163

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,800,814 B2
DATED : October 5, 2004
INVENTOR(S) : Tohru Ohsaka

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 3,

Line 14, "annulus" should read -- an annulus --;
Line 17, "power supply" should read -- signal --; and
Line 52, "annulus." should read -- an annulus. --.

Column 4,

Line 17, "annulus." should read -- an annulus. --.

Column 5,

Line 22, "plane. This" should read -- plane, this --.

Column 7,

Line 42, "gournd" should read -- ground --.

Column 9,

Line 1, "Further," should read -- Further, when --; and
Line 63, "layer 4" should read -- layer 14 --.

Column 10,

Line 9, "illustrated" should read -- illustrated, --;
Line 17, "layer 3," should read -- layer 13, --;
Line 37, "FIG. 15D," should read -- FIG. 5D, --;
Line 50, "part 7c" should read -- part 17c --; and
Line 59, "layer 4," should read -- layer 14, --.

Column 11,

Line 11, "26a to 26h" should read -- 16a to 16h --; and
Line 55, "second" should read -- third --.

Column 12,

Line 22, "part 2c" should read -- part 27c --; and
Line 38, "layer 11" should read -- layer 21 --.

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Column 15,

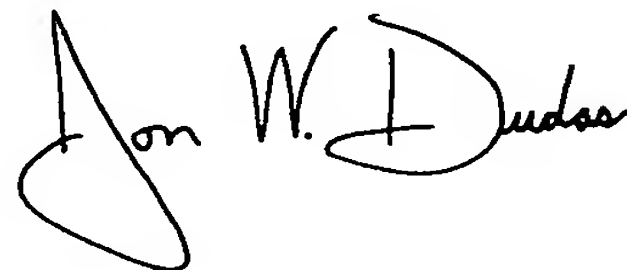
Line 53, "layer each at least having at least three wiring" should be deleted.

Column 16,

Line 11, "around" should read -- ground --.

Signed and Sealed this

First Day of March, 2005



JON W. DUDAS
Director of the United States Patent and Trademark Office